What is claimed is:

1. A data pad region of a liquid crystal display panel, comprising:

a plurality of data lines vertically arranged at specified intervals;

a plurality of data pads respectively connected to the data lines;

at least one first side contact with a first area formed in each data pad; and

at least one second side contact with a second area formed in each data pad, wherein the first area is larger than the second area.

2. The data pad region of claim 1, wherein the first side contact is positioned in a central portion of the data pad.

3. The data pad region of claim 2, wherein at least two second side contacts are respectively formed at one end of each data pad and at the other end of each data pad.

4. A method for fabricating a data pad region of a liquid crystal display panel, comprising:

forming a gate insulating layer, data lines and a passivation film in a data pad forming region of a substrate;

forming at least one first side contact hole with a first area at the central region of the data pad forming region and forming at least two second side contact holes with a second area

respectively at both edges of the data pad forming region, wherein the first area is larger than the second area; and

forming at least one first side contact electrically connecting one of the data lines to a conductive layer at the first side contact hole and forming at least one second side contact electrically contacting the data line to the conductive layer at the second side contact hole by patterning a conductive material.

5. The method of claim 4, wherein the passivation film is made of an organic material.

6. The method of claim 4, wherein the passivation film is made of BCB (benzocyclobutene).

7. The method of claim 4, wherein the passivation film is formed as a triple deposition structure of SiNx film/BCB (benzocyclobutene) film/SiNx film.

8. The method of claim 4, wherein the data line is made of Mo.

9. The method of claim 4, wherein the data line is etched by dry-etching.

10. The method of claim 4, wherein the gate insulating layer is exposed at the bottom

surfaces of the first side contact hole and the second side contact hole by dry-etching of the passivation film.

11. A data pad region of a liquid crystal display panel, comprising:

a substrate;

a gate insulating layer, data lines and a passivation film in a data pad forming region of the substrate, wherein the passivation film in the data pad forming region including at least one first side contact hole with a first area and at least one second side contact hole with a second area, wherein the first area is larger than the second area; and

at least one first side contact electrically connecting one of the data lines to a conductive layer at the first side contact hole and at least one second side contact electrically connecting the data line to the conductive layer at the second side contact hole.

12. The data pad region of claim 11, wherein the passivation film is made of an organic material.

13. The data pad region of claim 11, wherein the passivation film is made of BCB (benzocyclobutene).

14. The data pad region of claim 11, wherein the passivation film is formed as a triple

deposition structure of SiNx film/BCB (benzocyclobutene) film/SiNx film.

15. The data pad region of claim 11, wherein the data line is made of Mo.

16. A liquid crystal display panel, comprising:

a substrate having an image display region with unit pixels arranged in a matrix and a data pad region at the periphery of the image display region, wherein the data pad region includes:

a plurality of data lines vertically arranged at specified intervals;

a plurality of data pads respectively connected to the data lines;

at least one first side contact with a first area formed in each data pad; and

at least one second side contact with a second area formed in each data pad, wherein the first area is larger than the second area.